FIG. 1

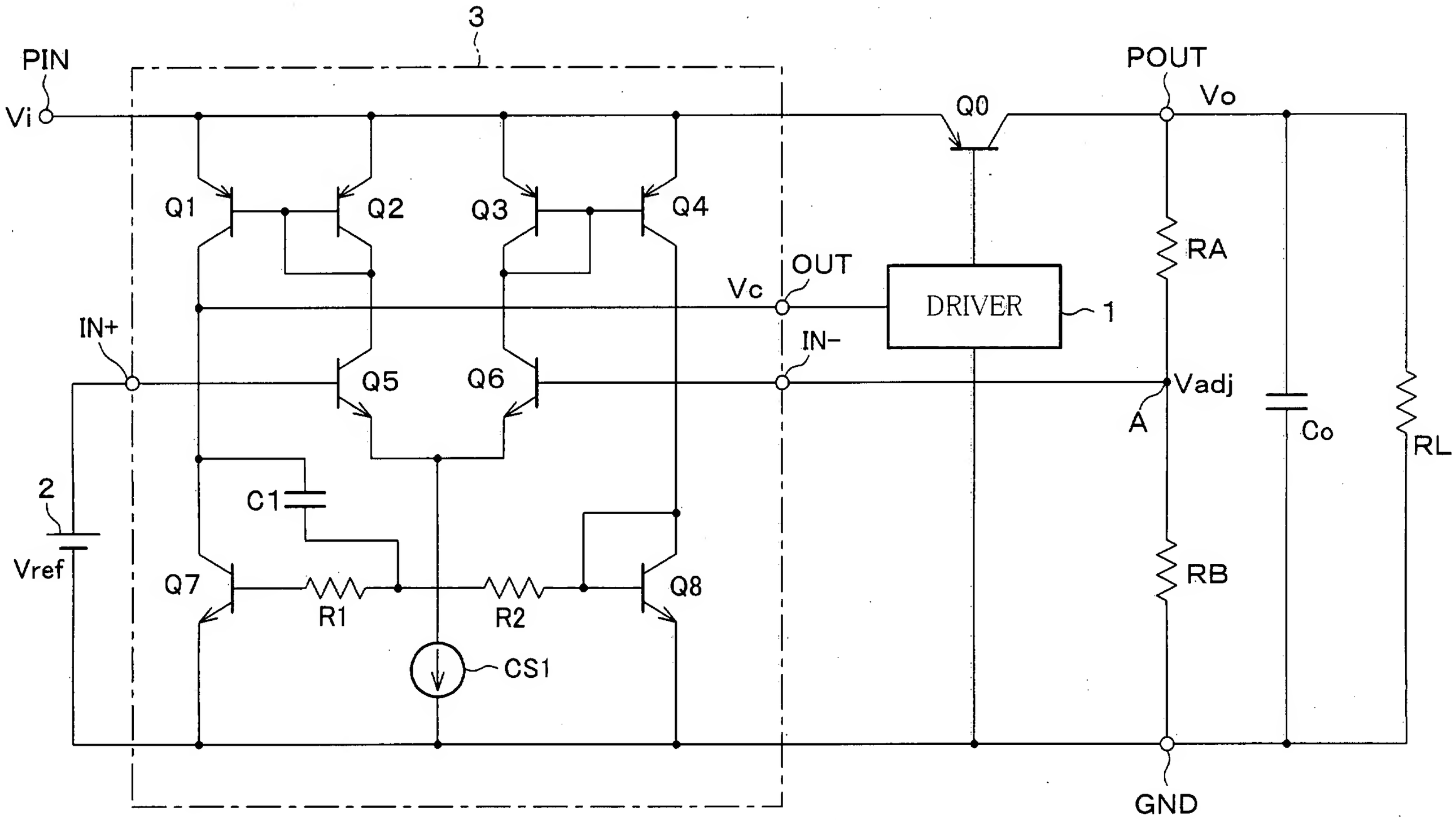

3
PIN
Vi
POUT
Vo
Q0
Q1
Q2
Q3
Q4
RA
OUT
Vc
DRIVER
1
IN+
IN-
Q5
Q6
Vadj
A
Co
RL
2
C1
Vref
Q7
Q8
R1
R2
RB
CS1
GND

FIG. 2

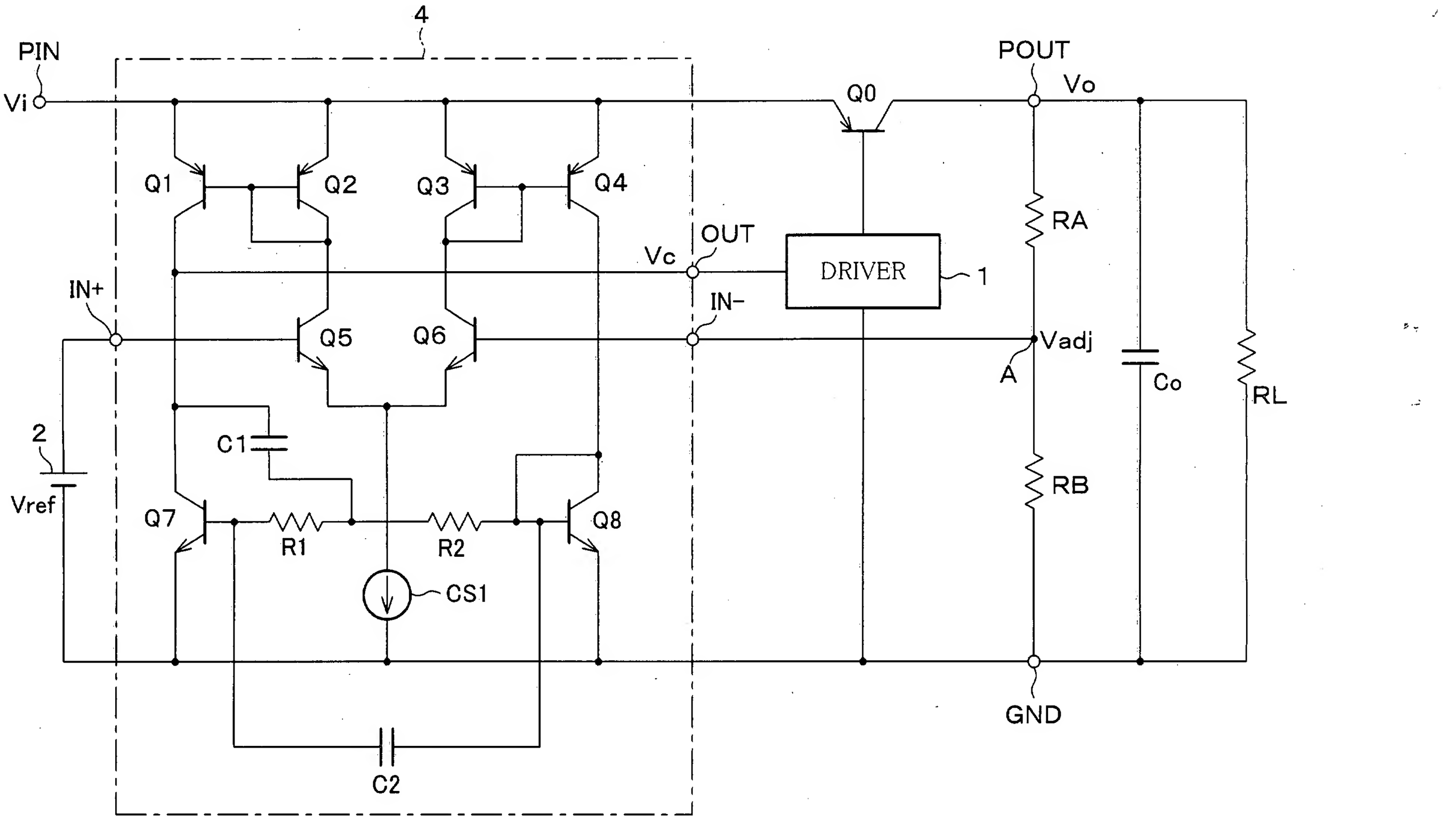

4
PIN
Vi
POUT
Vo
Q0
Q1
Q2
Q3
Q4
RA
OUT
Vc
DRIVER
1
IN+
IN−
Q5
Q6
Vadj
A
Co
RL
2
C1
RB
Vref
Q7
R1
R2
Q8
CS1
C2
GND

FIG. 3

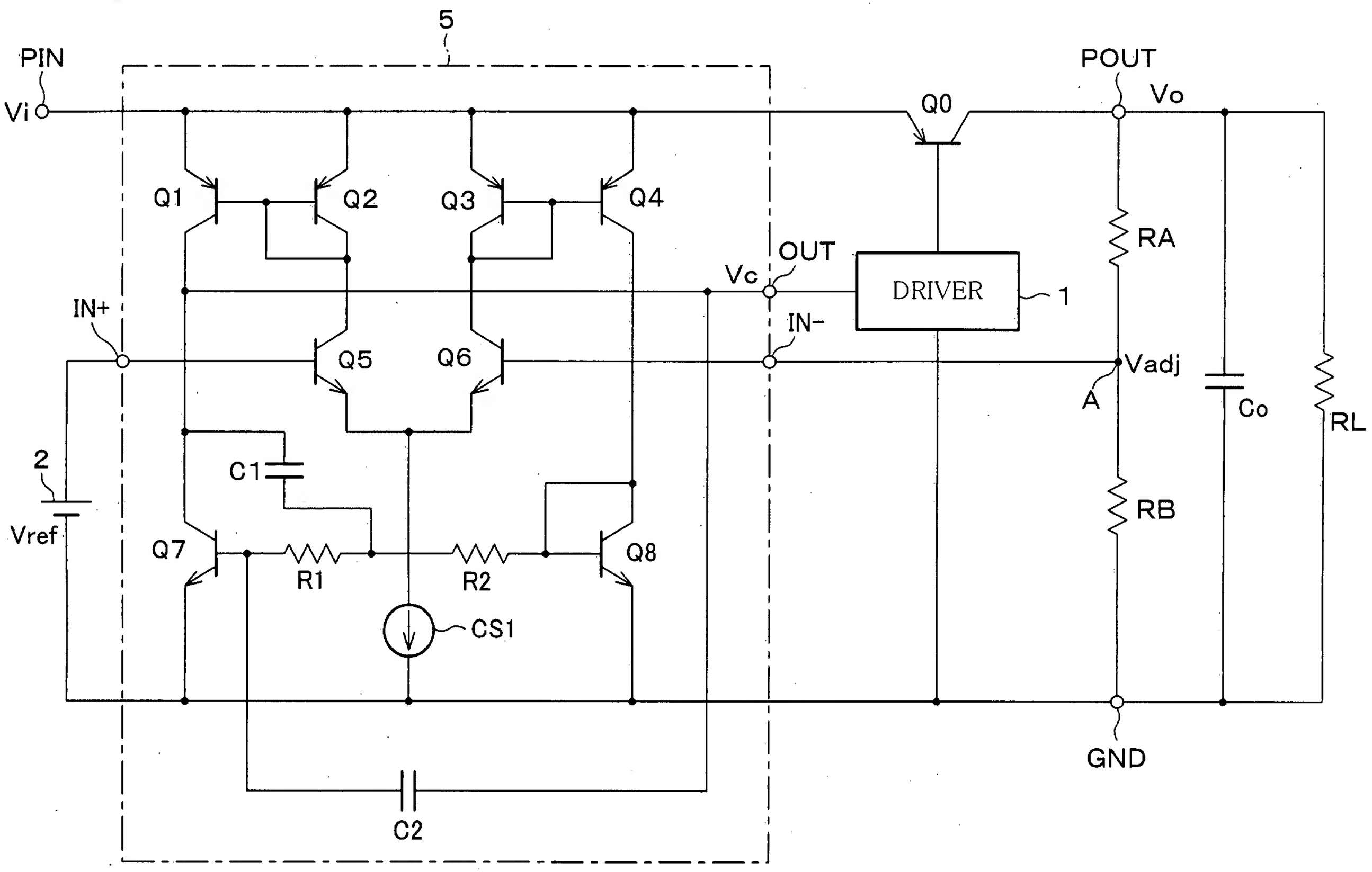

5
PIN
Vi
POUT
Vo
Q0
Q1
Q2
Q3
Q4
RA
OUT
Vc
DRIVER
1
IN+
IN-
Q5
Q6
Vadj
A
Co
RL
2
C1
RB
Vref
Q7
Q8
R1
R2
CS1
GND
C2

FIG. 4

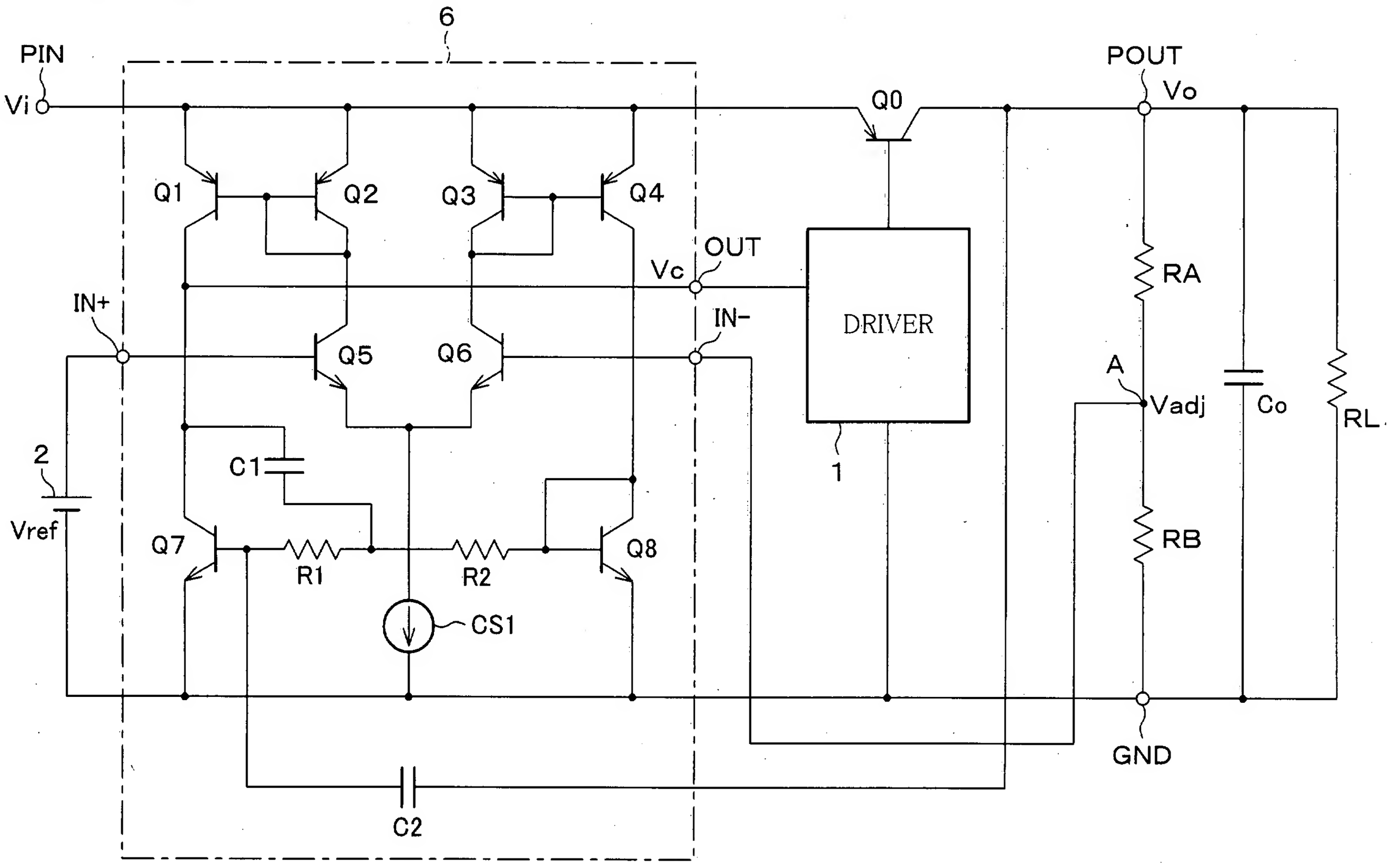

6
PIN
Vi
Q0
POUT
Vo
Q1
Q2
Q3
Q4
OUT
Vc
RA
IN+
IN-
DRIVER
Q5
Q6
A
Vadj
Co
RL
2
C1
1
Vref
Q7
Q8
R1
R2
RB
CS1
GND
C2

FIG. 5

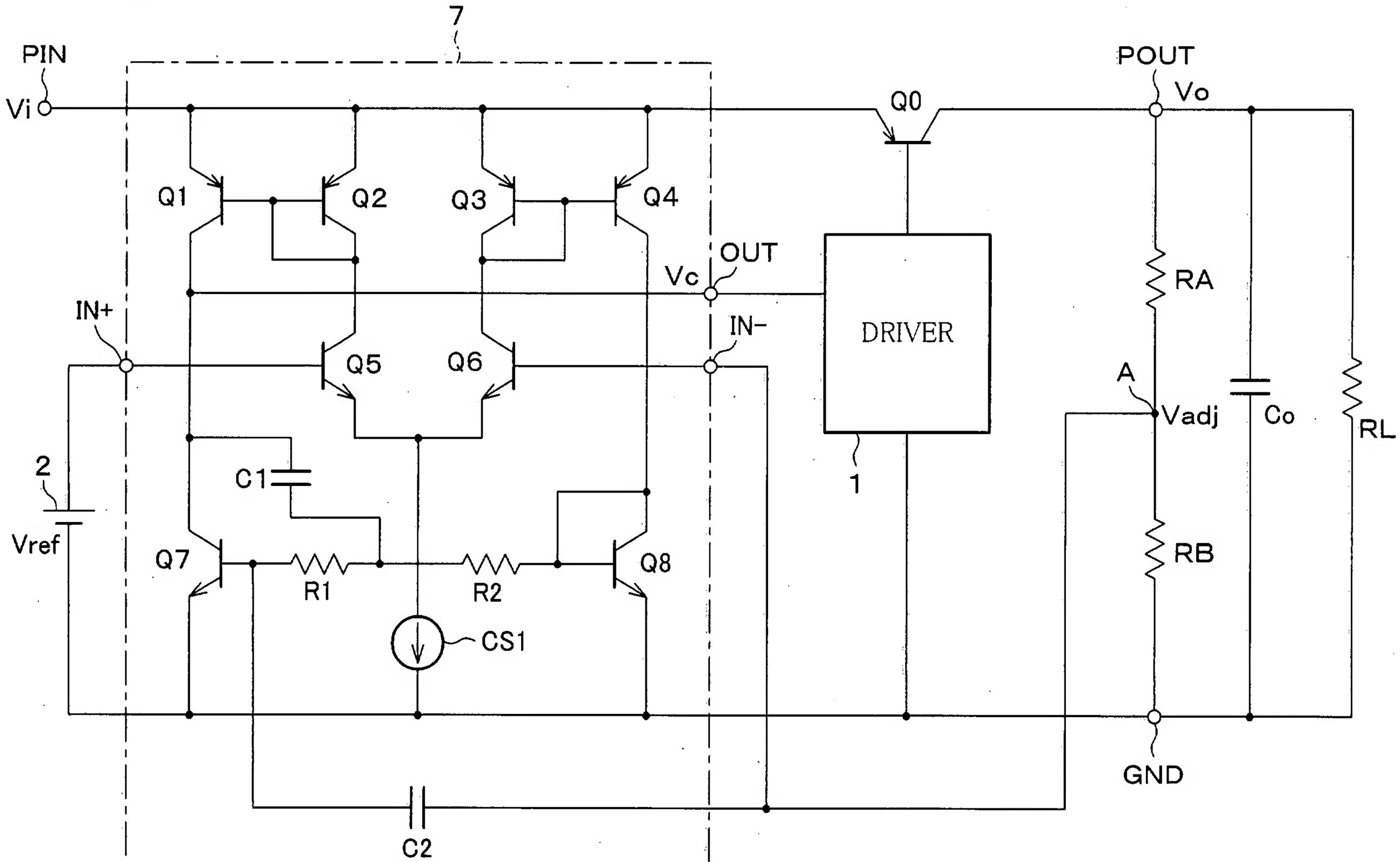

7
PIN
Vi
POUT
Vo
Q0
Q1
Q2
Q3
Q4
Vc
OUT
IN+
IN−
DRIVER
RA
Q5
Q6
A
Vadj
Co
RL
2
C1
1
Vref
Q7
Q8
R1
R2
RB
CS1
GND
C2

FIG. 6
(PRIOR ART)

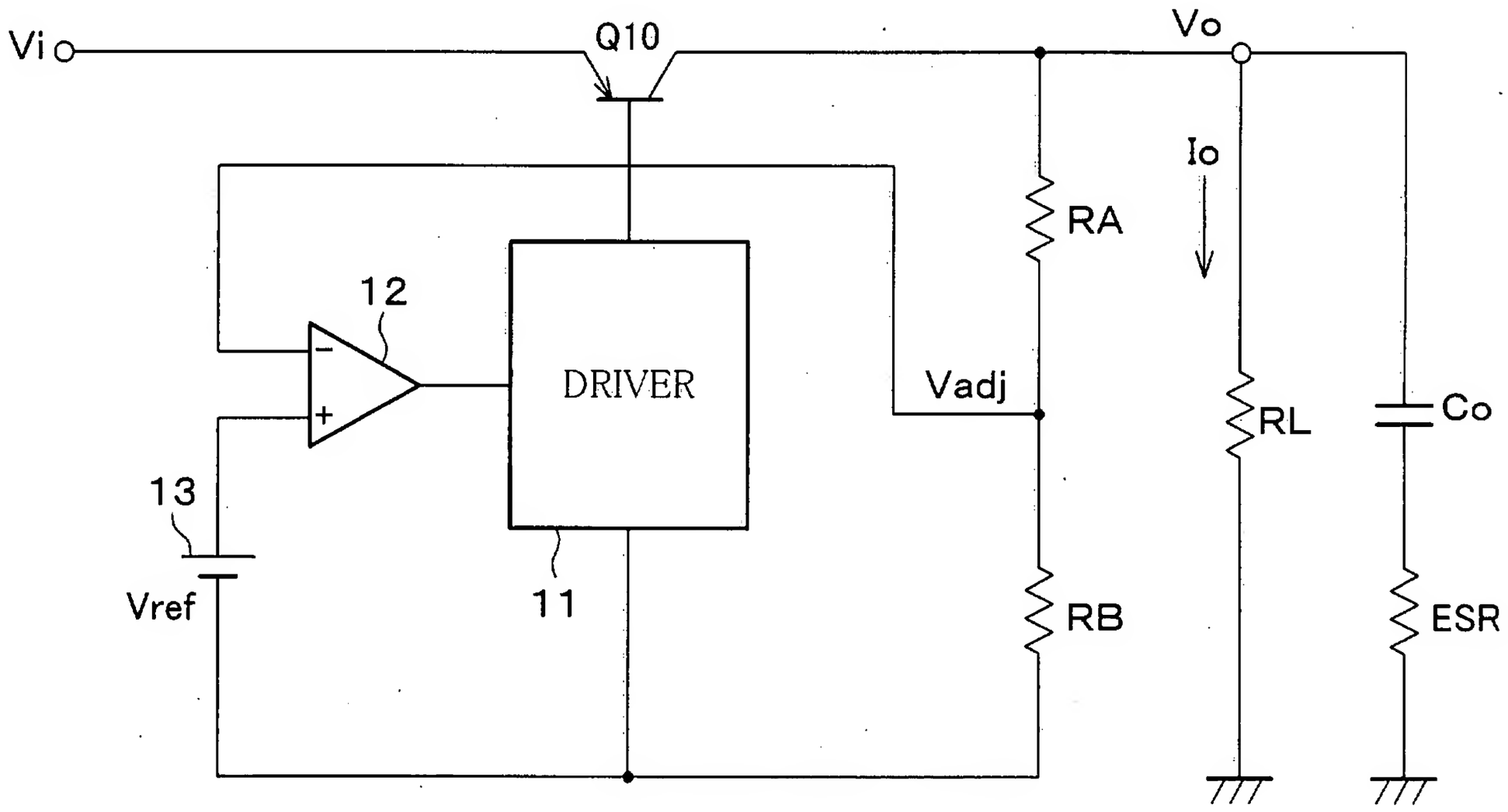


FIG. 7
(PRIOR ART)

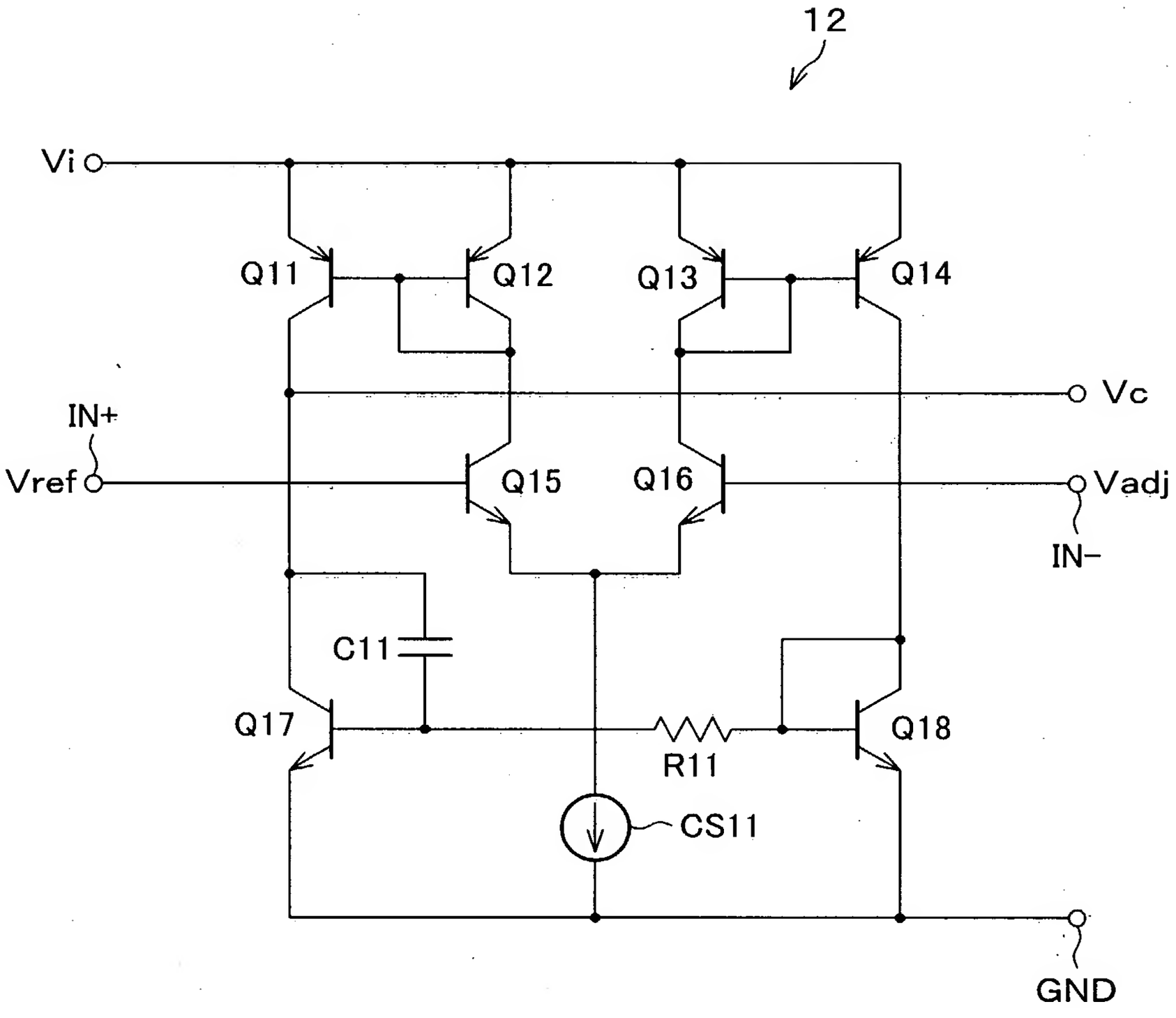


FIG. 8

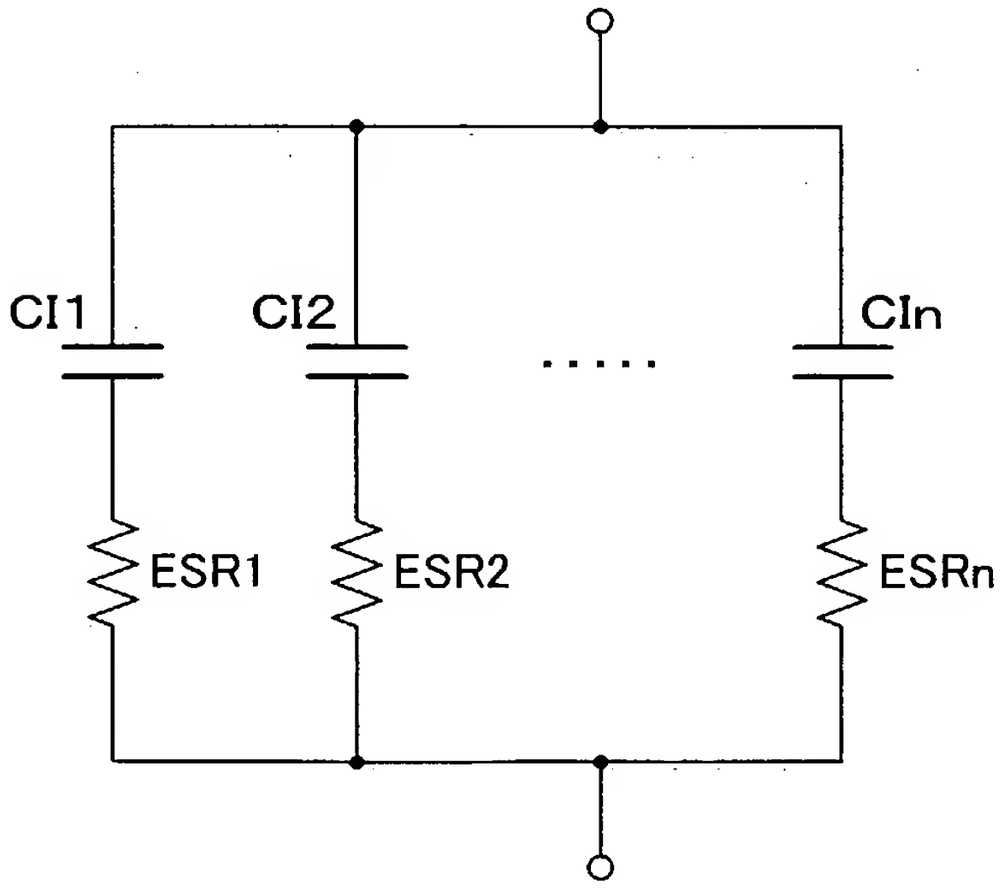


FIG. 9

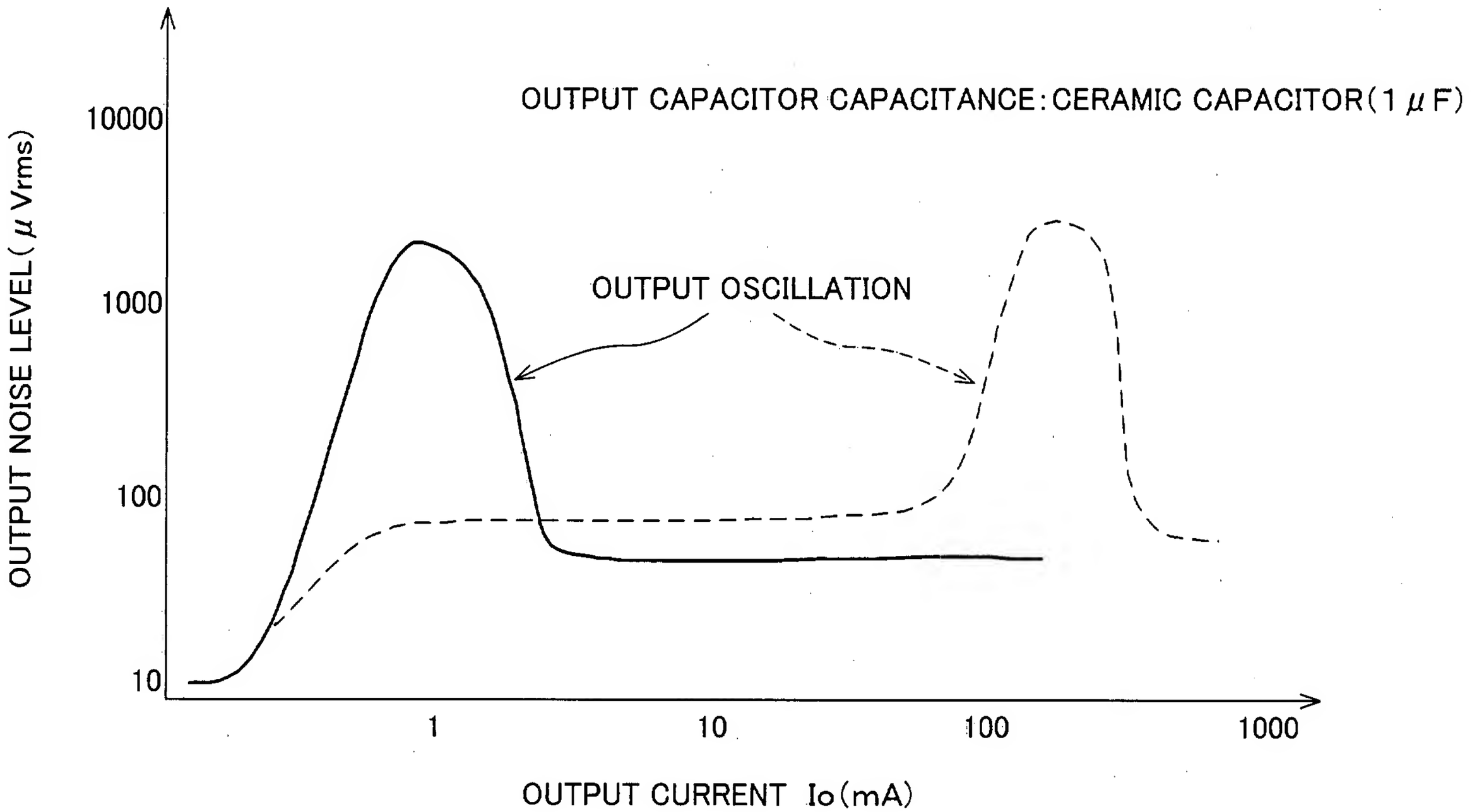
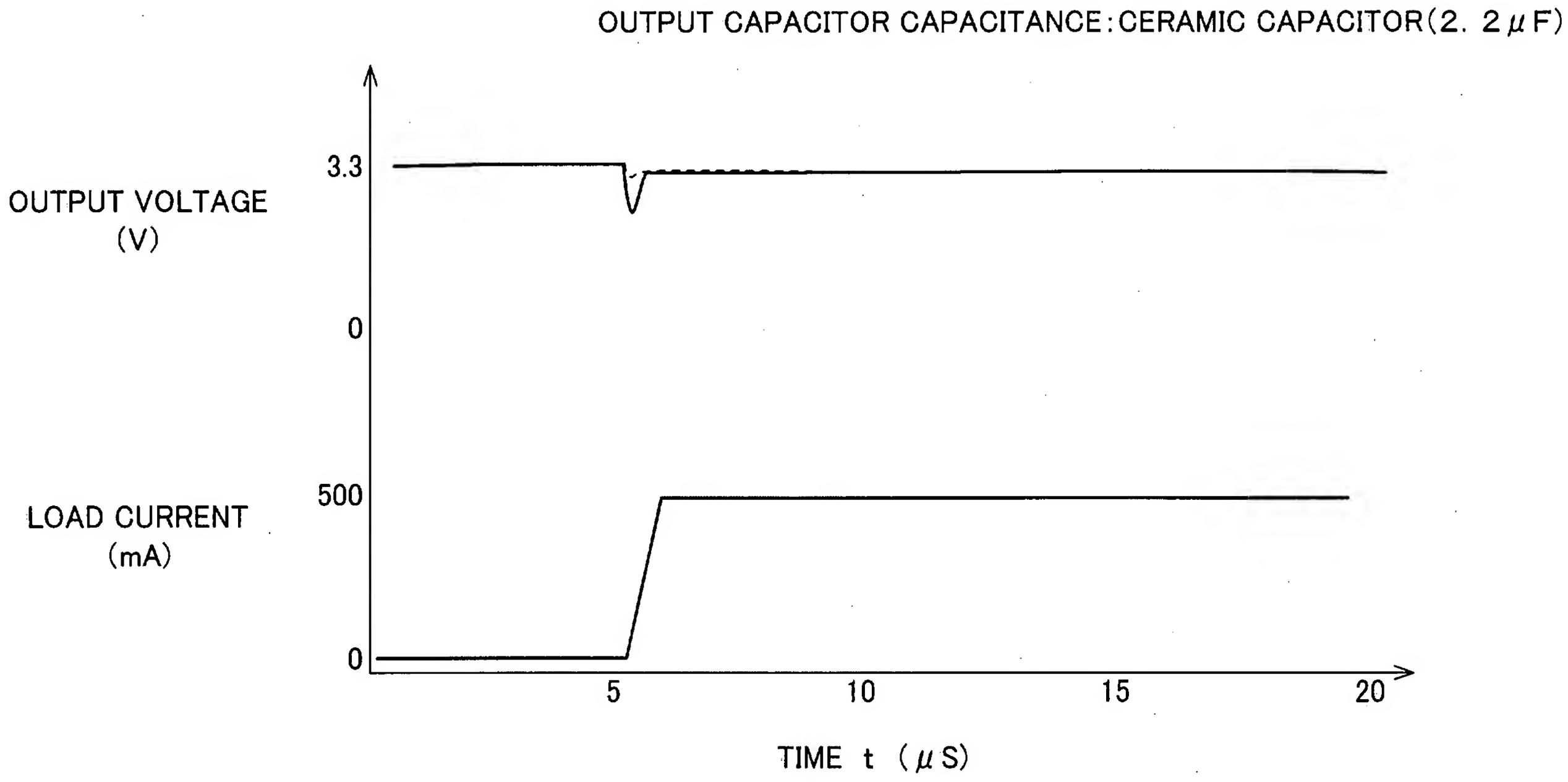

OUTPUT CAPACITOR CAPACITANCE : CERAMIC CAPACITOR (1 μ F)
OUTPUT OSCILLATION
OUTPUT NOISE LEVEL (μ Vrms)
10000
1000
100
10
1
10
100
1000
OUTPUT CURRENT Io (mA)

FIG. 10


OUTPUT CAPACITOR CAPACITANCE : CERAMIC CAPACITOR (2. 2 μ F)
OUTPUT VOLTAGE
(V)
3.3
0
LOAD CURRENT
(mA)
500
0
5
10
15
20
TIME t (μ S)